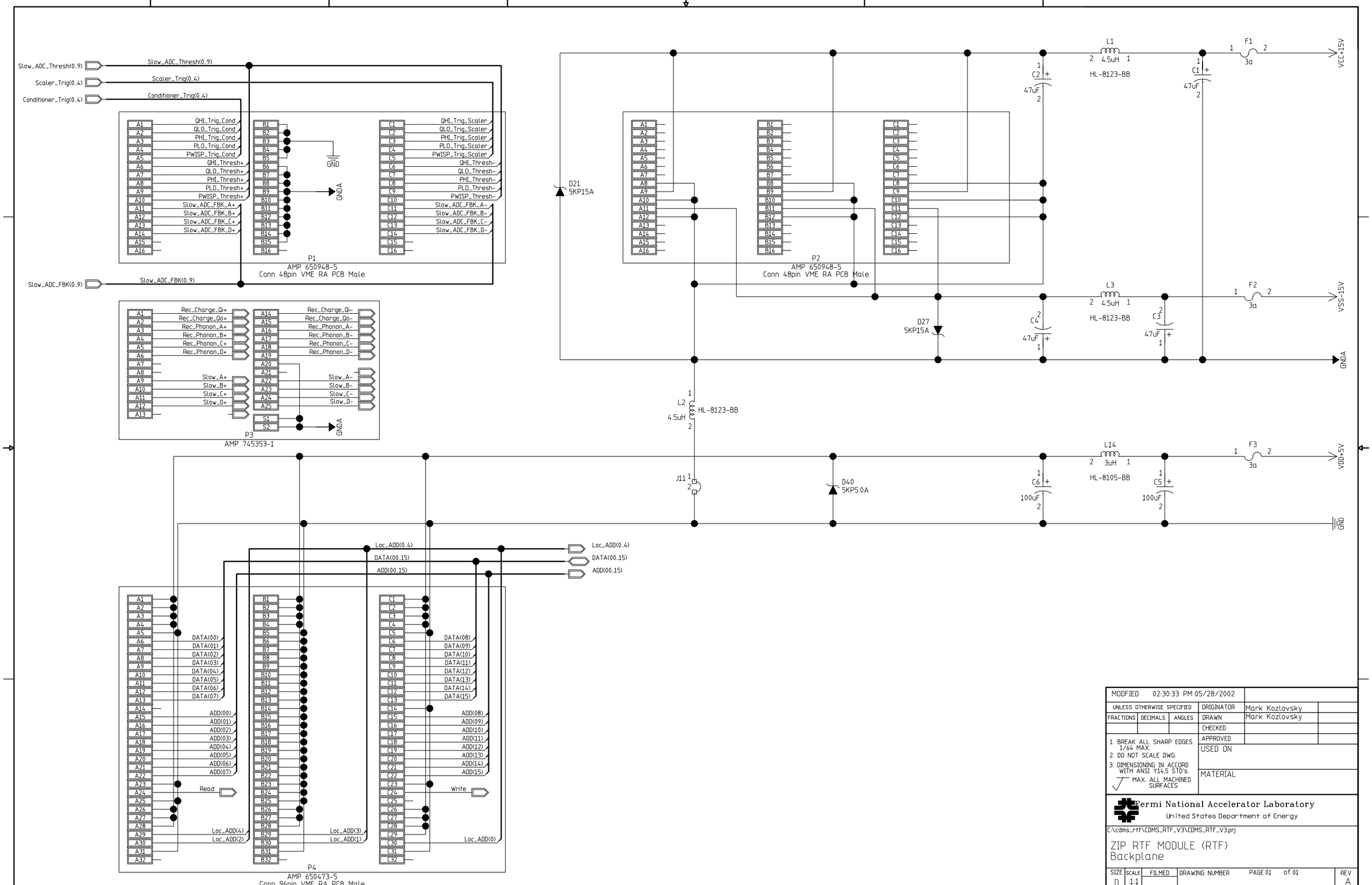
Slow_ADC_Thresh(0..9)
Scaler_Trig(0..4)
Conditioner_Trig(0..4)
Slow_ADC_FBK(0..9)
QHI_Trig_Cond
QLO_Trig_Cond
PHI_Trig_Cond
PLO_Trig_Cond
PWISP_Trig_Cond
QHI_Thresh+
QLO_Thresh+
PHI_Thresh+
PLO_Thresh+
PWISP_Thresh+
Slow_ADC_FBK_A+
Slow_ADC_FBK_B+
Slow_ADC_FBK_C+
Slow_ADC_FBK_D+
QHI_Trig_Scaler
QLO_Trig_Scaler
PHI_Trig_Scaler
PLO_Trig_Scaler
PWISP_Trig_Scaler
QHI_Thresh-
QLO_Thresh-
PHI_Thresh-
PLO_Thresh-
PWISP_Thresh-
Slow_ADC_FBK_A-
Slow_ADC_FBK_B-
Slow_ADC_FBK_C-
Slow_ADC_FBK_D-
GND
GNDA
P1
AMP 650948-5
Conn 48pin VME RA PCB Male
P2
AMP 650948-5
Conn 48pin VME RA PCB Male
Rec_Charge_Qi+
Rec_Charge_Qo+
Rec_Phonon_A+
Rec_Phonon_B+
Rec_Phonon_C+
Rec_Phonon_D+
Rec_Charge_Qi-
Rec_Charge_Qo-
Rec_Phonon_A-
Rec_Phonon_B-
Rec_Phonon_C-
Rec_Phonon_D-
Slow_A+
Slow_B+
Slow_C+
Slow_D+
Slow_A-
Slow_B-
Slow_C-
Slow_D-
P3
AMP 745353-1
D21
5KP15A
D27
5KP15A
L1
4.5uH
HL-8123-BB
L2
4.5uH
HL-8123-BB
L3
4.5uH
HL-8123-BB
L14
3uH
HL-8105-BB
C1
47uF
C2
47uF
C3
47uF
C4
47uF
C5
100uF
C6
100uF
F1
3a
F2
3a
F3
3a
VCC+15V
VSS-15V
VDD+5V
J11
D40
5KP5.0A
Loc_ADD(0..4)
DATA(00..15)
ADD(00..15)
DATA(00)
DATA(01)
DATA(02)
DATA(03)
DATA(04)
DATA(05)
DATA(06)
DATA(07)
DATA(08)
DATA(09)
DATA(10)
DATA(11)
DATA(12)
DATA(13)
DATA(14)
DATA(15)
ADD(00)
ADD(01)
ADD(02)
ADD(03)
ADD(04)
ADD(05)
ADD(06)
ADD(07)
ADD(08)
ADD(09)
ADD(10)
ADD(11)
ADD(12)
ADD(13)
ADD(14)
ADD(15)
Read
Write
Loc_ADD(4)
Loc_ADD(2)
Loc_ADD(3)
Loc_ADD(1)
Loc_ADD(0)
P4
AMP 650473-5
Conn 96pin VME RA PCB Male
MODIFIED 02:30:33 PM 05/28/2002
UNLESS OTHERWISE SPECIFIED
FRACTIONS
DECIMALS
ANGLES
ORIGINATOR Mark Kozlovsky
DRAWN Mark Kozlovsky
CHECKED
APPROVED
1. BREAK ALL SHARP EDGES 1/64 MAX.
2. DO NOT SCALE DWG.
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD'S.
MAX. ALL MACHINED SURFACES
USED ON
MATERIAL
Fermi National Accelerator Laboratory
United States Department of Energy
C:\cdms_rtf\CDMS_RTF_V3\CDMS_RTF_V3.prj
ZIP RTF MODULE (RTF)
Backplane
SIZE D
SCALE 1:1
FILMED
DRAWING NUMBER
PAGE 01 of 01
REV A
H=8ND, V=4LA