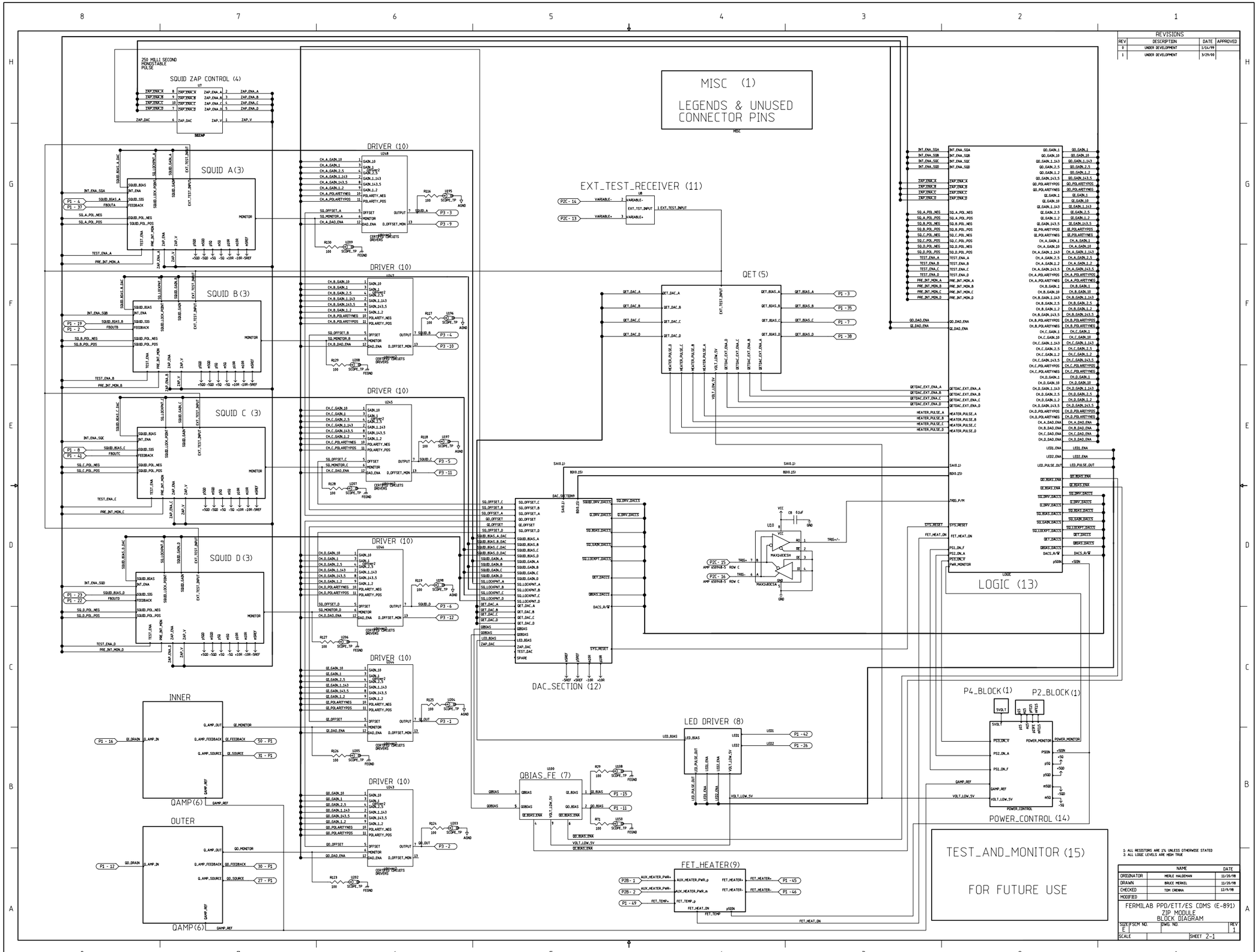

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
0	UNDER DEVELOPMENT	1/14/99	
1	UNDER DEVELOPMENT	3/29/00	

1: ALL RESISTORS ARE 1% UNLESS OTHERWISE STATED
2: ALL LOGIC LEVELS ARE MEM TRUE

	NAME	DATE
ORIGINATOR	MERLE HALDEMAN	11/20/98
DRAWN	BRUCE MERKEL	11/20/98
CHECKED	TOM CRENNA	12/9/98
MODIFIED		

FERMILAB PPD/ETT/ES COMS (E-891)
ZIP MODULE
BLOCK DIAGRAM

SIZE	FSCM NO.	DWG. NO.	REV
E			1
SCALE		SHEET 2-1	