

LOCKIN 1:

1. Test connections:

- Observe LOCKIN output at LOCKIN 1 OUT on front panel and at the LOCKIN 1 output on the Breakout Box.
- Measure PS1 DC OUT at PS1 DC OUT on front panel and at the PS1 DC output on the Breakout Box.
- Set up a frequency generator for an output of 1khz , 100mV that is frequency modulated at 100hz, 20% duty cycle.
- Connect the frequency generator output to TEST POINT 1 located at the junction of C72 and R220.

2. Power up settings:

LOCKIN BYPASS 1:	0(off)
AZ FAST:	1(on)
V AMP FAST:	1(on)
EXT REF 1:	0(off)

3. Test preparation settings:

readfile lockin.macro

Turn off Lockin bypass: *bypass(0)*
 Set Az Fast on: *azFast*
 Set ext ref off: *extref(0)*

LOCKIN BYPASS 1:	0(off)
AZ FAST:	1(on)
V AMP FAST:	1(on)
EXT REF 1:	0(off)

4. LOCKIN 1 OUT test:

Input settings in Step 3 before starting test.

Is Lockin 1 Out a sine wave? _____
 What is its frequency? _____
 What is PS1 DC out's level? _____

Adjust the amplitude of the 1kHz carrier.

Does the amplitude of PS1 DC OUT change? _____
 Is Lockin 1 Out affected? _____

5. EXTERNAL REFERENCE test:

Input settings in Step 3 before starting test.

Turn on External Ref: *extref*

LOCKIN BYPASS 1:	0(off)
AZ FAST:	1(on)
V AMP FAST:	1(on)
EXT REF 1:	0(off)

Does the output change? _____
 How does it change. _____

Turn off External Ref: *extref(0)*

BLIP TESTING PROCEDURE

CDMS

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LOCKIN: BOARD # _____

STATUS _____

DATE _____

Does the original output return? _____

6. AZ FAST/SLOW test:

Input settings in Step 3 before starting test.

Observe PS1 DC Out and note how it reacts to changes in amplitude of the 1 kHz carrier.

LOCKIN BYPASS 1:	0(off)
AZ FAST:	1(on)
V AMP FAST:	1(on)
EXT REF 1:	0(off)

Does the output quickly zero out? _____

Turn Autozero Fast off: *azSlow*

Does the output slowly zero out? _____

Turn Autozero Fast on: *azFast*

LOCKIN 2:

1. Test connections:

- Observe LOCKIN output at LOCKIN 2 OUT on front panel and at the LOCKIN 2 output on the Breakout Box.
- Measure PS2 DC OUT at PS2 DC OUT on front panel and at the PS2 DC output on the Breakout Box.
- Set up a frequency generator for an output of 1khz , 100mV that is frequency modulated at 100hz, 20% duty cycle.
- Connect the frequency generator output to TEST POINT 2 located at the junction of C66 R199.

2. Power up settings:

LOCKIN BYPASS 2:	0(off)
AZ FAST:	1(on)
V AMP FAST:	1(on)
EXT REF 2:	0(off)

3. Test preparation settings:

readfile lockin.macro

Turn off Lockin bypass: *bypass(0)*
 Set Auto zero Fast on: *azFast*
 Set external ref off: *extRef2(0)*

LOCKIN BYPASS 2:	0(off)
AZ FAST:	1(on)
V AMP FAST:	1(on)
EXT REF 2:	0(off)

4. LOCKIN 2 OUT test:

Input settings in Step 3 before starting test.

Is Lockin 2 Out a sine wave? _____
 What is its frequency? _____
 What is PS2 DC out's level? _____

Adjust the amplitude of the 1kHz carrier.

Does the amplitude of PS2 DC OUT change? _____
 Is Lockin 2 Out affected? _____

5. EXTERNAL REFERENCE test:

Input settings in Step 3 before starting test.

Turn on External Ref: *extref*

LOCKIN BYPASS 2:	0(off)
AZ FAST:	1(on)
V AMP FAST:	1(on)
EXT REF 2:	0(off)

Does the output change? _____
 How does it change. _____

Turn off External Ref: *extref(0)*

BLIP TESTING PROCEDURE

CDMS

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LOCKIN: BOARD # _____

STATUS _____

DATE _____

Does the original output return? _____

6. AZ FAST/SLOW test:

Input settings in Step 3 before starting test.

Observe PS2 DC Out and note how it reacts to changes
in amplitude of the 1 kHz carrier.

LOCKIN BYPASS 2:	0(off)
AZ FAST:	1(on)
V AMP FAST:	1(on)
EXT REF 2:	0(off)

Does the output quickly zero out? _____

Turn Auto zero Fast off: *azSlow*

Does the output slowly zero out? _____

Turn Autozero Fast on: *azFast*