Relay Rack 06 N01 (Trigger Logic)

N1S01 BNC 8020 Pulse Generator	N1S02 LeCroy 429A 2X8	N1S03 LeCroy 429A 2 X 8 Fan	N1S04 LeCroy 623B Octal Disc.	N1S05 LRS365AL Logic Unit	N1S06 LRS222 Gate Generator	N1S07 Joevger Gate Generator	N1S08 LRS365AL Logic Unit	N1S09 Phillips 710 Octal Disc.	N1S10 LRS335 Quad Amp.	N1S11 (Empty)	N1S12 Level Adapter LRS688AL
Trig out: not con− nected Pulse out Ch 1 to C1S12 Ch2 Pulse out Ch 2 to C1S12 Ch 2	In: from C1S12 Out 1: Calib. "Pulse Gen" RR03 Out 2: Rich LED In: "pre−scaled" from C1S12 out 1 Out: PP7C	In: FastBeam Out: "Fast Beam to Mu Trig" C1S20 Scaler C1: S9Ch11 S11Ch11 TPC Busy N1S07 Ch 1 start Ch 2 start RFD 04 C1S13 In: N1S9Ch0 (Disc) N1S4Ch5 RF −> RR03C1S4 Ch14 +4 Out to N1S12 Nim Inputs 0−3 $\overline{\text{Out}}$: PPB3 N1S12 Nim In	1) In: PP7D Out: N1S5 upper C1S13 2) In: PP6D Out: N1S5 upper C1S13 3) In: PP5D N1S5 upper C1S13 4) In: PP4D Out: N1S5 upper C1S13 6) In: N1S3 Out: N1S5 lower 7) In: N1S6 Out: N1S5 lower	In: A −> S04 Ch1 B −> S04 Ch2 C −> S04 Ch3 D −> S04 Ch4 Co Lev =1 No Vetos Out: Mult−trig N02S05 Ch1 input: B In: A −> narrow RF (5ns) S04Ch6 B −> S04Ch7 Out: N02S08 stop upper Co Lev =2 Veto CD	Start: End of PPC Busy N2S9 $\overline{\text{Out}}$ Del: N1S04 Ch6 Start: No Start NIMout: PPB7	Start: N01S02 Upper Fanout Delay: N01S08 Upper InputA Start: TPC Gate Busy N01S03 Upper Fanout Gate: Beam Spill RR03	Upper: Input A −− N01S07 Upper Delay Veto BCD Coincidence 1 Out: C1S12 In 0 C1S13 in 16 Lower: Empty	0) In: N01S10 Ch 0 Output Out: N01S03 Lower Input 1) Unused 2) In: N2S6 $\overline{\text{Out}}$: Ch3 3)In: Ch2 Out: PP A5 4)In: N2S6 $\overline{\text{Out}}$: Ch5 5)In: Ch4 Out: PPA6 6)In: N2S6 $\overline{\text{Out}}$: Ch7 7)In: Ch6 Out: PPA7	Ch0: In: RF Signal Out: N1S9 Ch0 Ch1−3: Unused		Upper: Nim: N1S3 Ch.4 outs 1−4 TTL: TPCA−D Clk Nim in: N1S3Ch4 TTL out: Beam PPC Clk VME S3 NORM Lower: Nim:: N3S10 (4) Nim Out: N2S9 TTL: TPC 0,4,B,C IRQ Busy TTL Out: Beam PPC Busy 2 VME S3 COMPL