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PSI43 standard testing procedure

1. Make sure that Buffer Board has no power on it (two LEDs indicating +5V and -5V power have to be off).
2. Place the wire probe on to the chip under test.
3. Run script file PSI43_standard and observe scope signals. During the setup, the chip should respond at least with UltraBlack, Black and Last DAC programmed pulses at the output. The Last DAC programmed pulse height will change during the setup.
4. When the script comes to the I2C address setting, verify that two hit data is present (see example)
5. If two hits are not present, adjust WBC register and CalDel register values until two hits are present.
6. Adjust Vref pot until the charge value in both hits is maximized.
7. Adjust VhldDel register until charge value in both hits is maximized.
8. Quit manual adjustment loop and finish the measurements with ADC.
9. Store file with appropriate name corresponding to the chip ID.