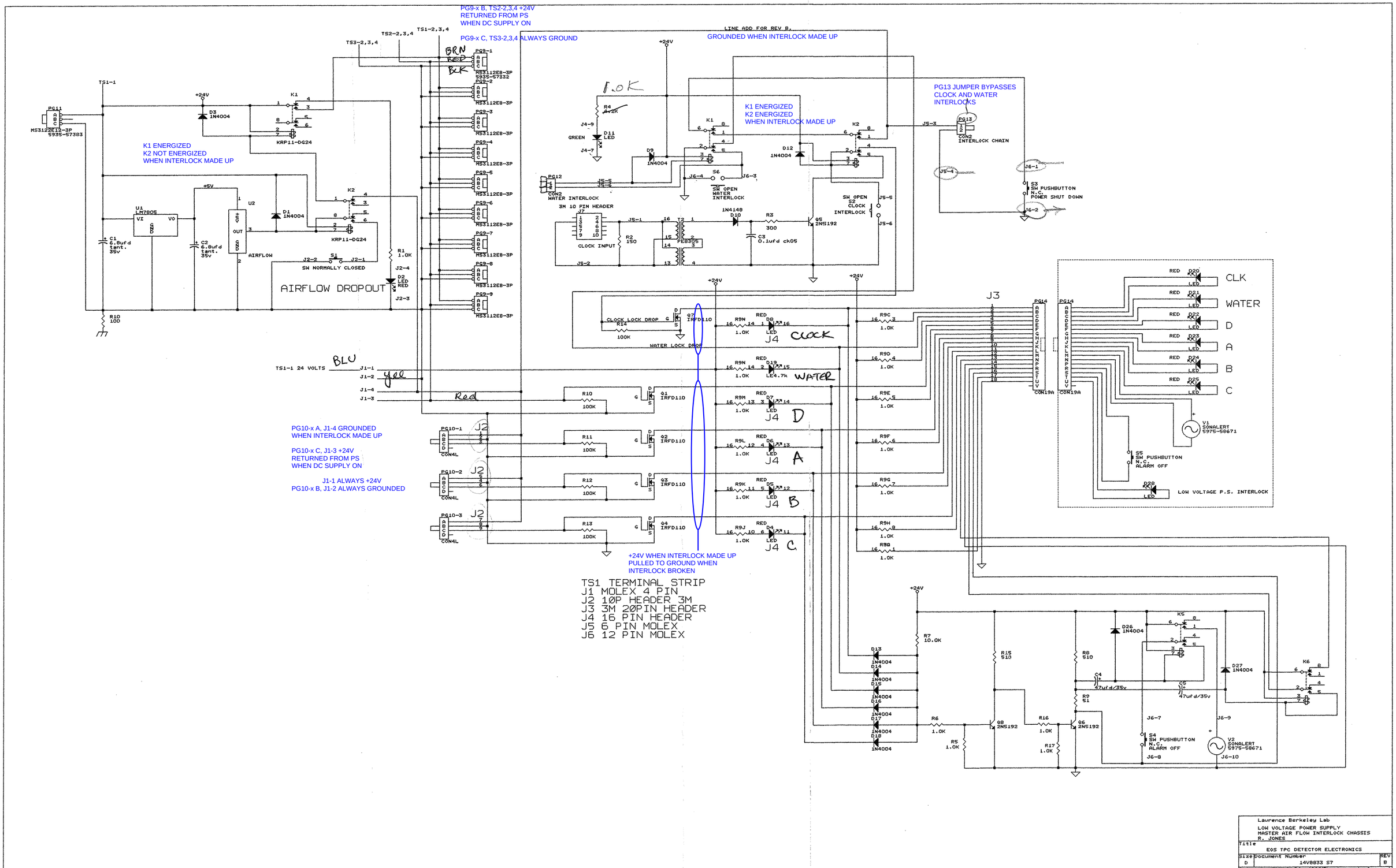
PG9-x A, TS1-2,3,4 +24V
SENT TO PS
WHEN INTERLOCK MADE UP
PG9-x B, TS2-2,3,4 +24V
RETURNED FROM PS
WHEN DC SUPPLY ON
PG9-x C, TS3-2,3,4 ALWAYS GROUND
LINE ADD FOR REV B,
GROUNDED WHEN INTERLOCK MADE UP
K1 ENERGIZED
K2 ENERGIZED
WHEN INTERLOCK MADE UP
PG13 JUMPER BYPASSES
CLOCK AND WATER
INTERLOCKS
K1 ENERGIZED
K2 NOT ENERGIZED
WHEN INTERLOCK MADE UP
AIRFLOW DROPOUT
SW NORMALLY CLOSED
SW OPEN
WATER
INTERLOCK
SW OPEN
S2
CLOCK
INTERLOCK
S3
SW PUSHBUTTON
N.C.
POWER SHUT DOWN
CLOCK
WATER
D
A
B
C
LOW VOLTAGE P.S. INTERLOCK
S5
SW PUSHBUTTON
N.C.
ALARM OFF
S4
SW PUSHBUTTON
N.C.
ALARM OFF
PG10-x A, J1-4 GROUNDED
WHEN INTERLOCK MADE UP
PG10-x C, J1-3 +24V
RETURNED FROM PS
WHEN DC SUPPLY ON
J1-1 ALWAYS +24V
PG10-x B, J1-2 ALWAYS GROUNDED
+24V WHEN INTERLOCK MADE UP
PULLED TO GROUND WHEN
INTERLOCK BROKEN
TS1 TERMINAL STRIP
J1 MOLEX 4 PIN
J2 10P HEADER 3M
J3 3M 20PIN HEADER
J4 16 PIN HEADER
J5 6 PIN MOLEX
J6 12 PIN MOLEX
Laurence Berkeley Lab
LOW VOLTAGE POWER SUPPLY
MASTER AIR FLOW INTERLOCK CHASSIS
R. JONES
Title
EOS TPC DETECTOR ELECTRONICS
Size D
Document Number 14V8833 S7
REV B
Date: March 24, 1992
Sheet 1 of 1